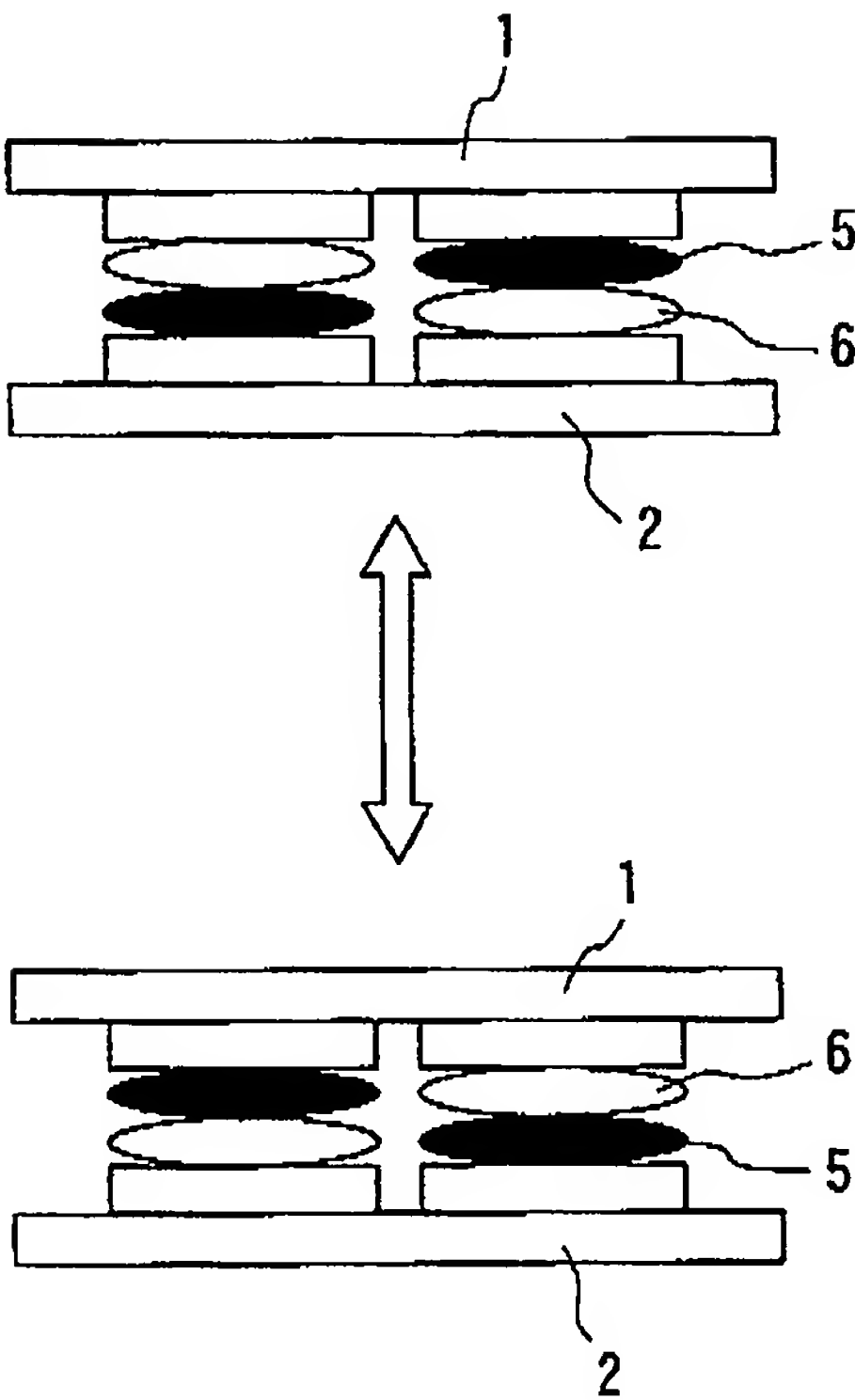
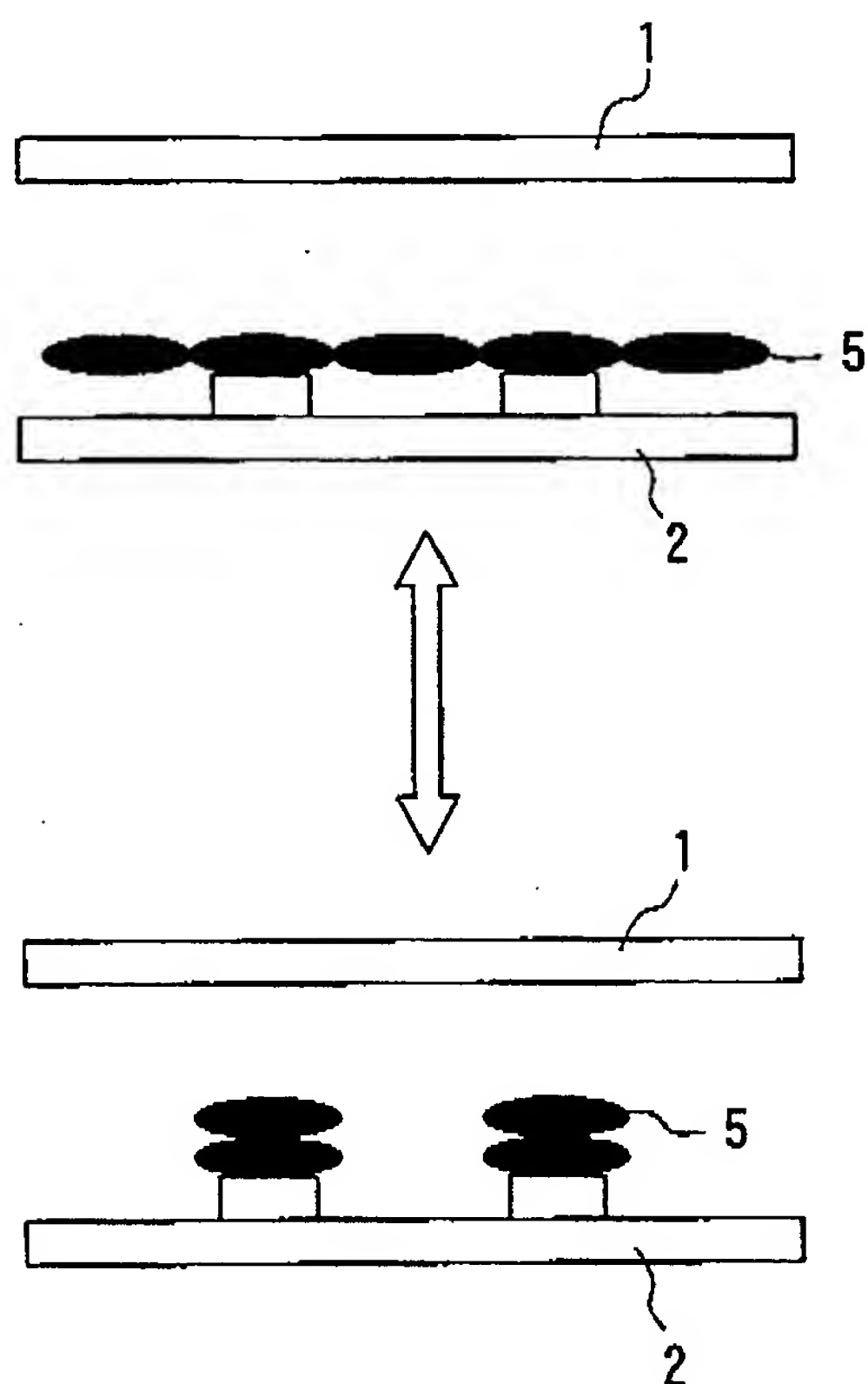


FIG. 1



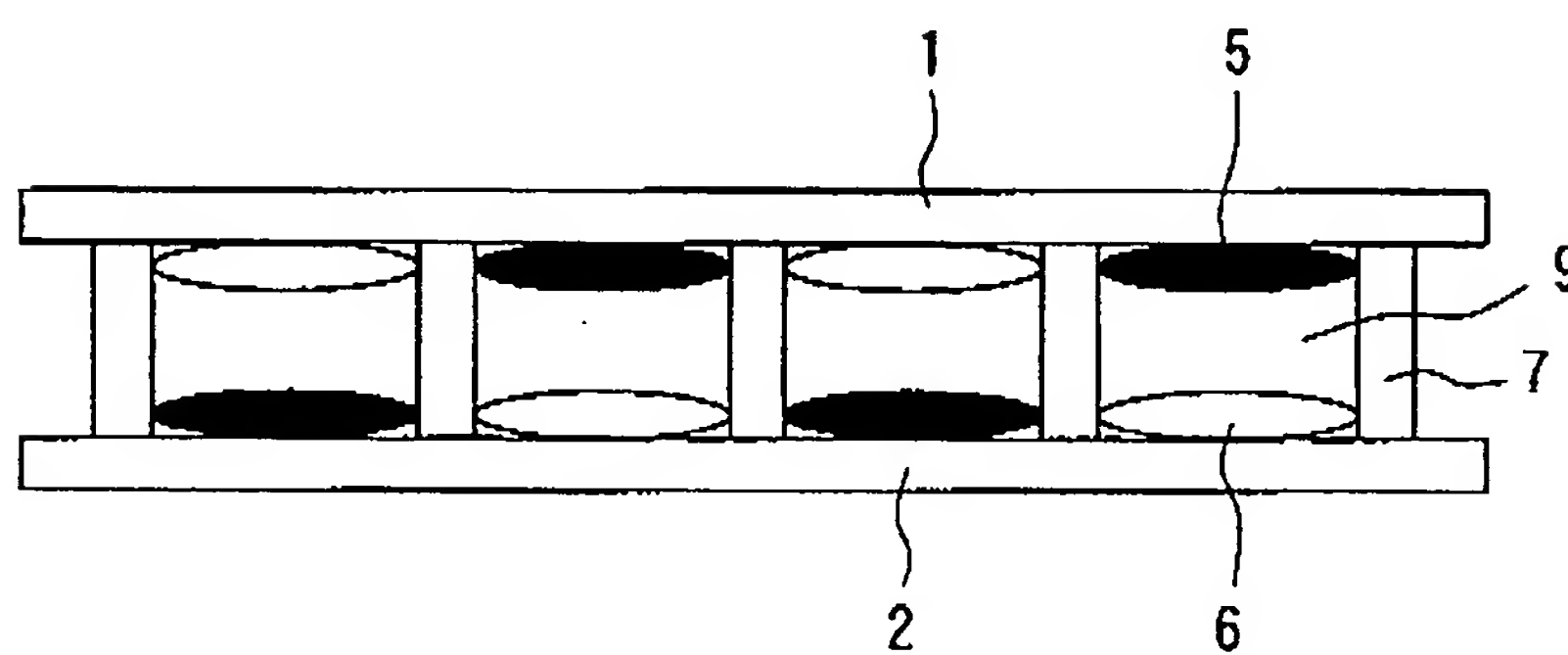
2/19

FIG. 2



3/19

FIG. 3



4/19

FIG. 4a

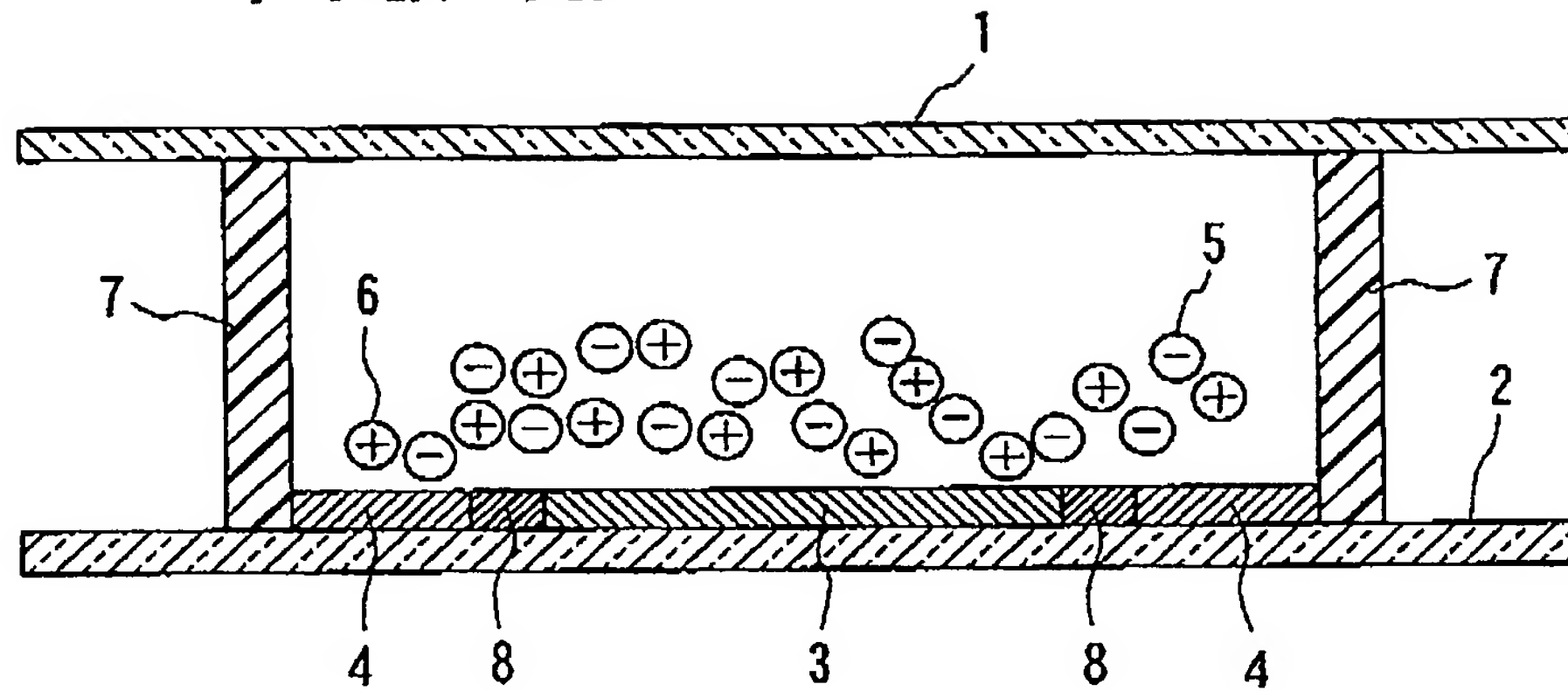


FIG. 4b

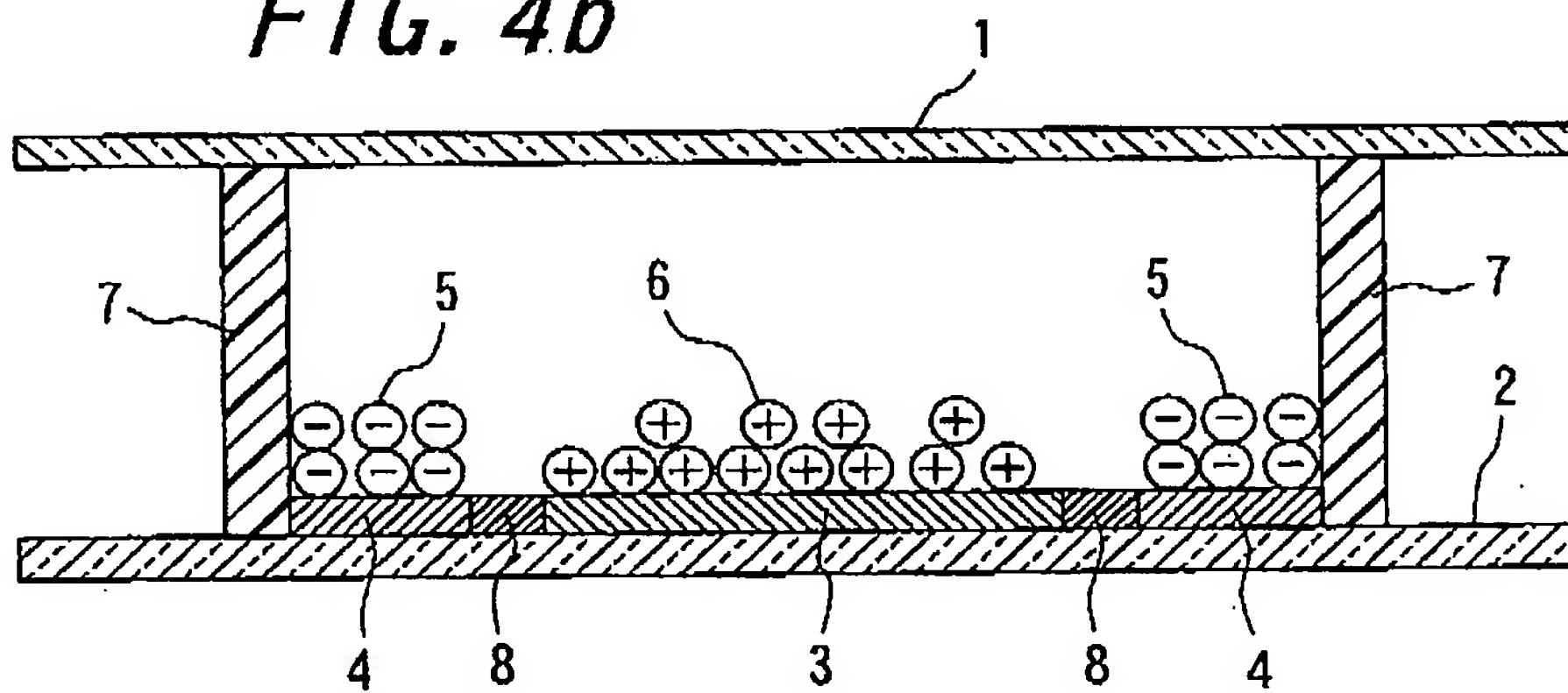


FIG. 4c

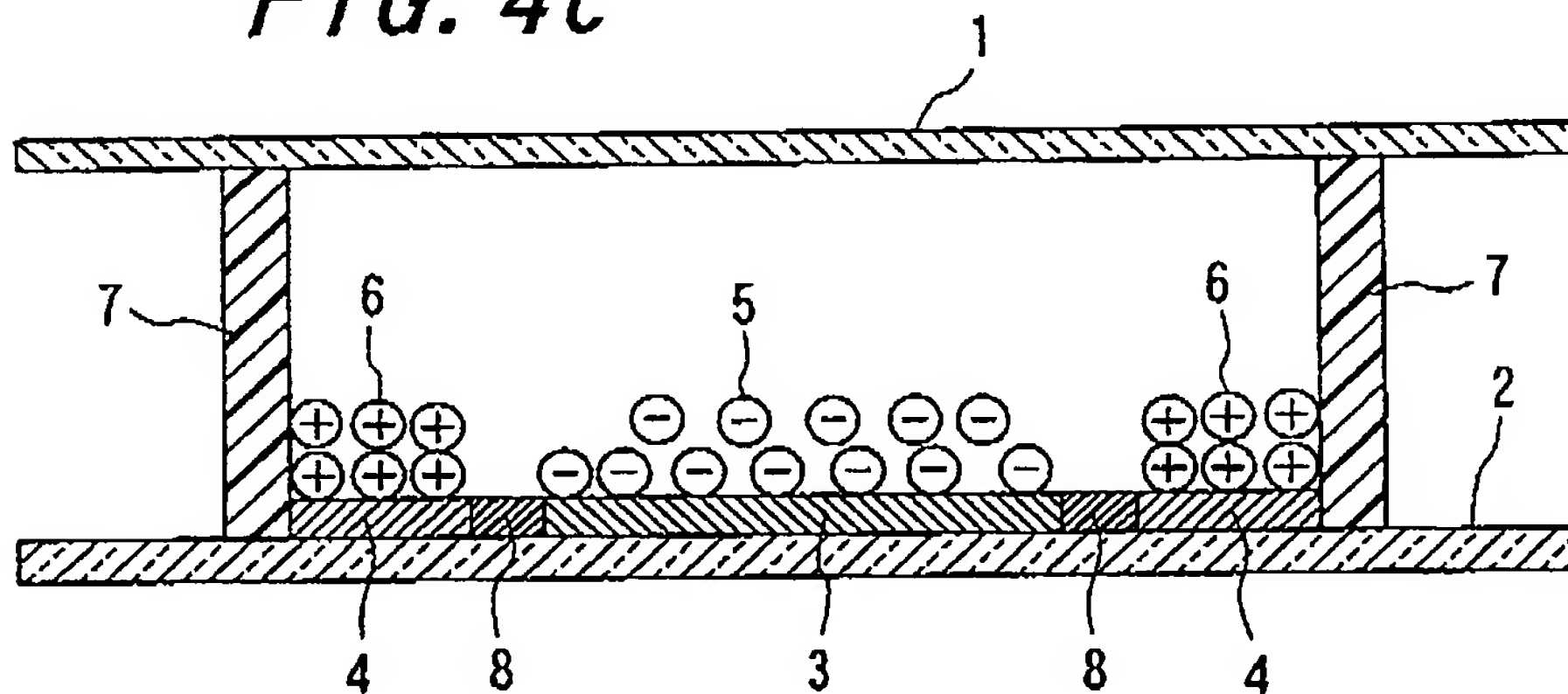


FIG. 5

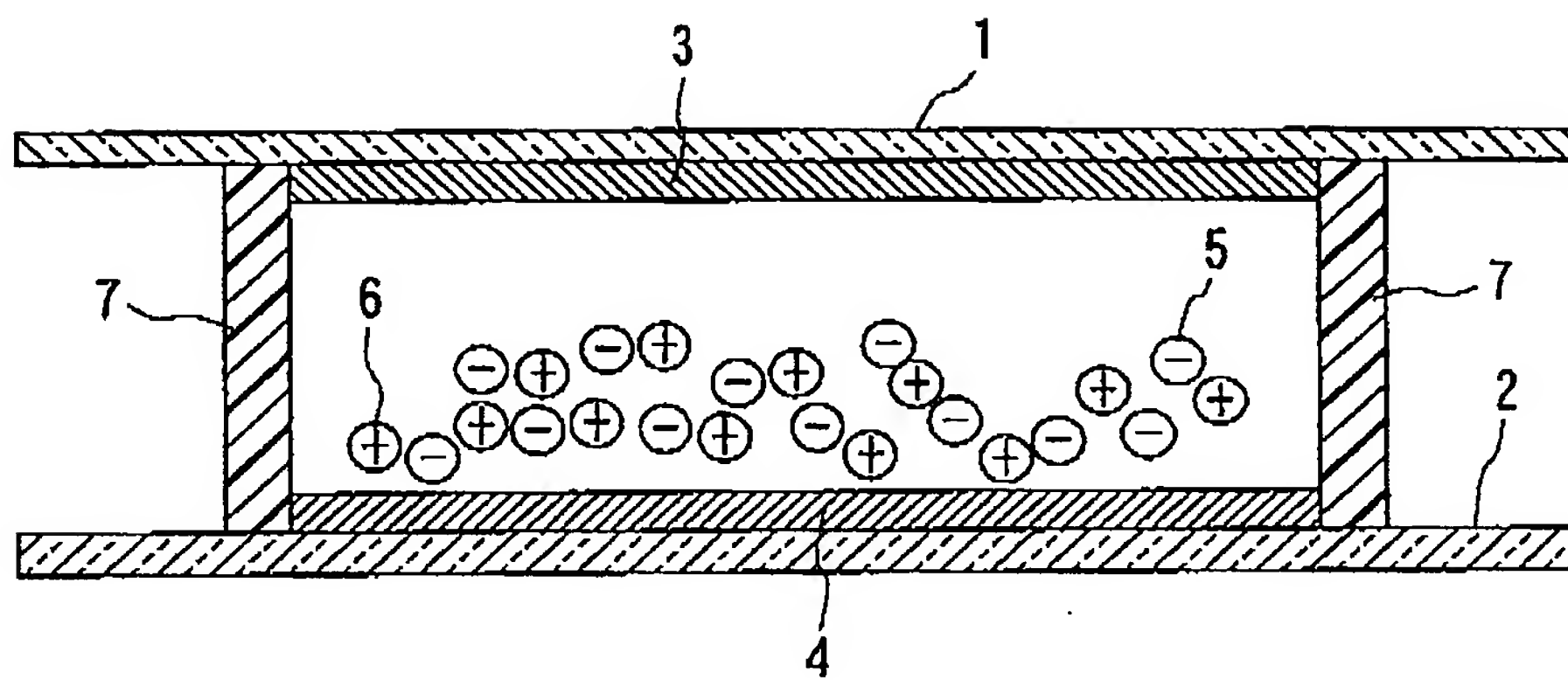


FIG. 6

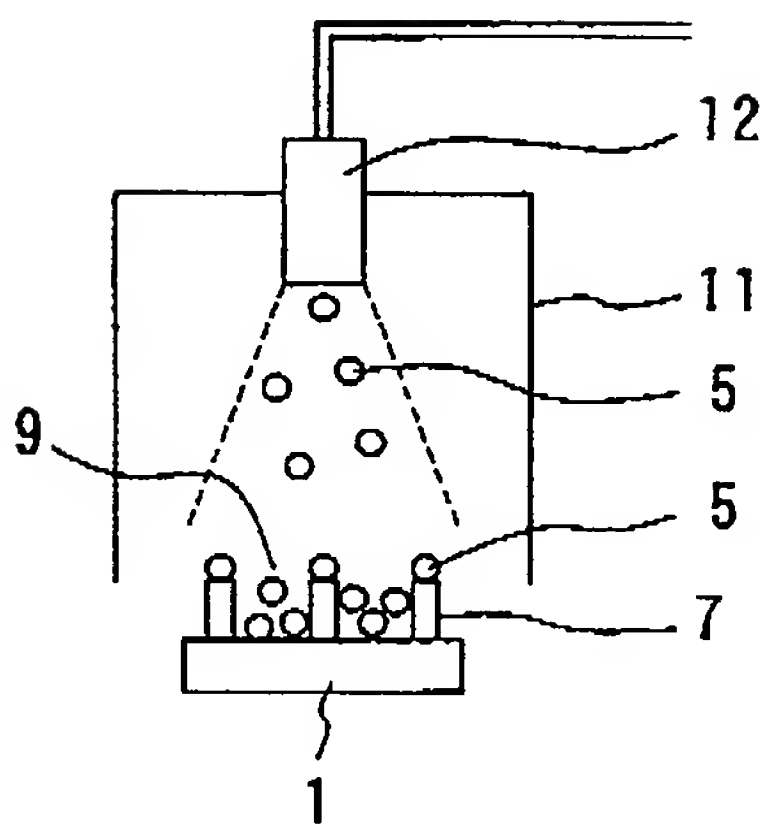


FIG. 7

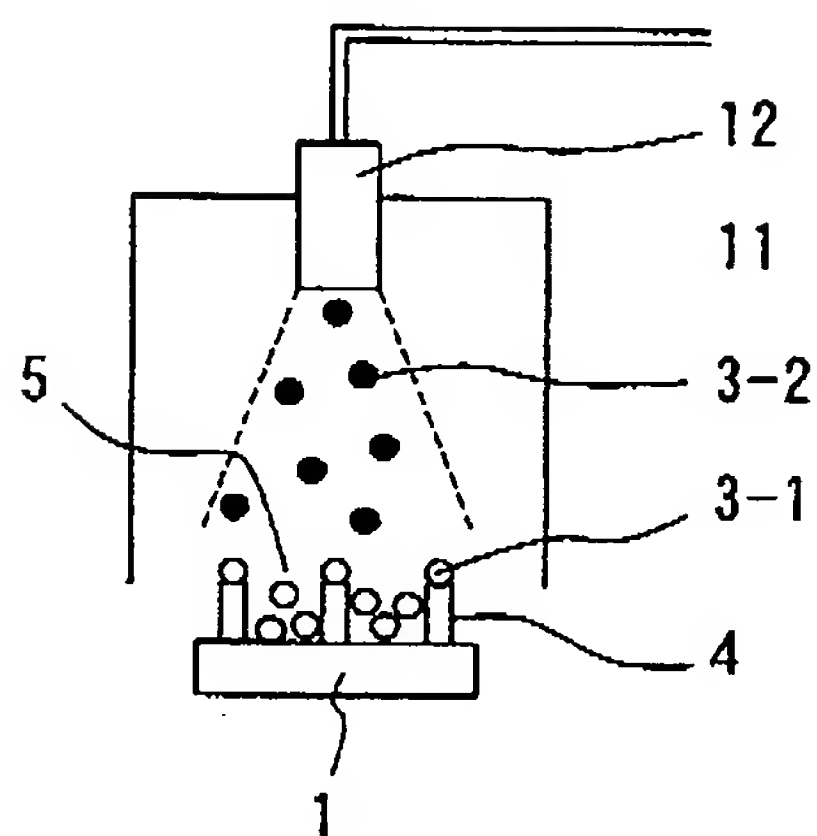


FIG. 8

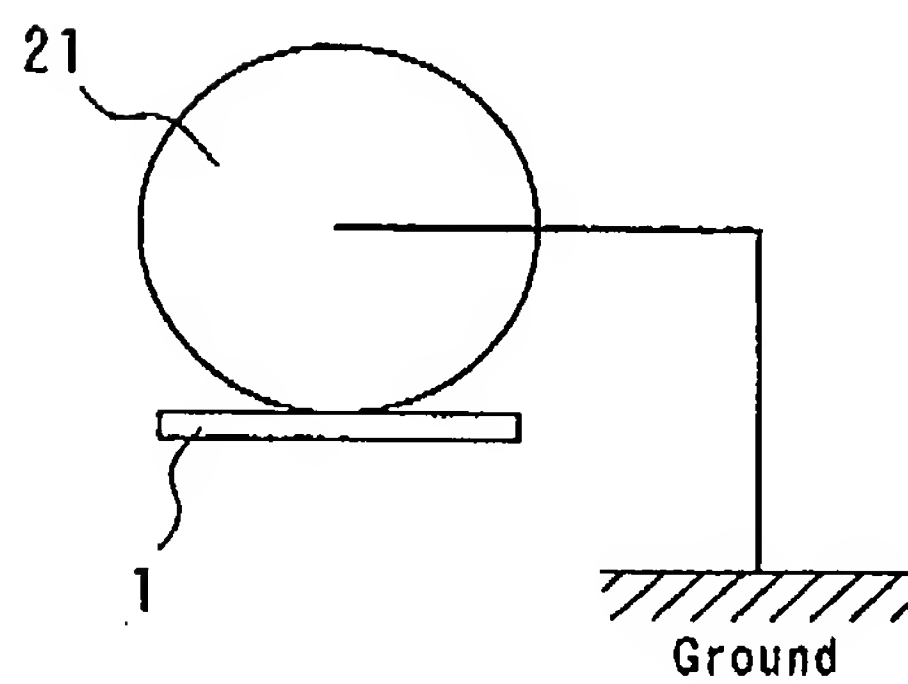


FIG. 9a

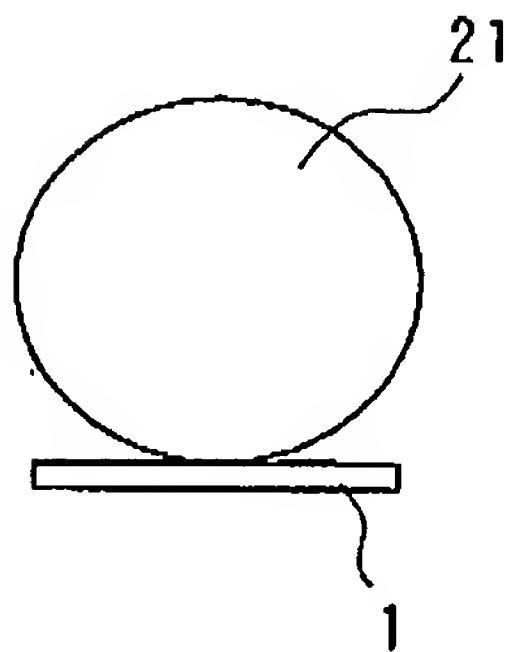


FIG. 9b

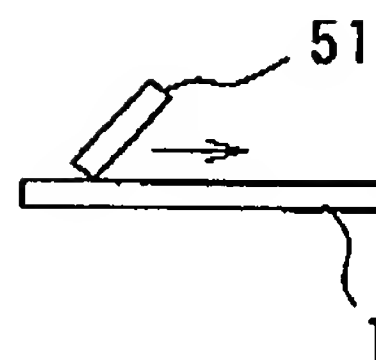


FIG. 9c

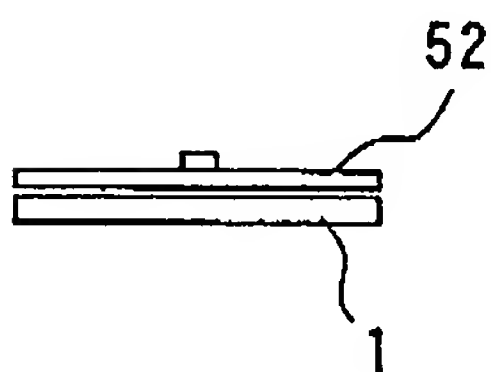


FIG. 9d

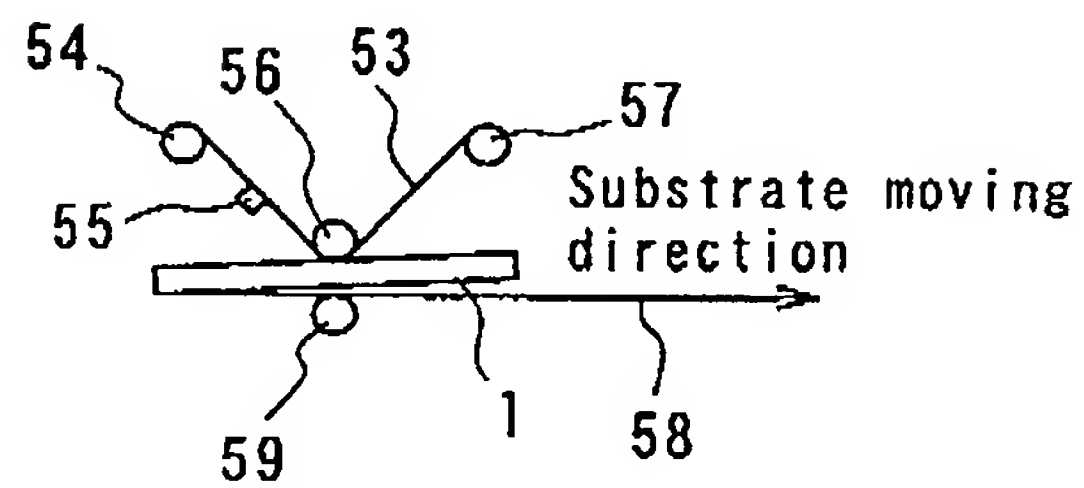




FIG. 10

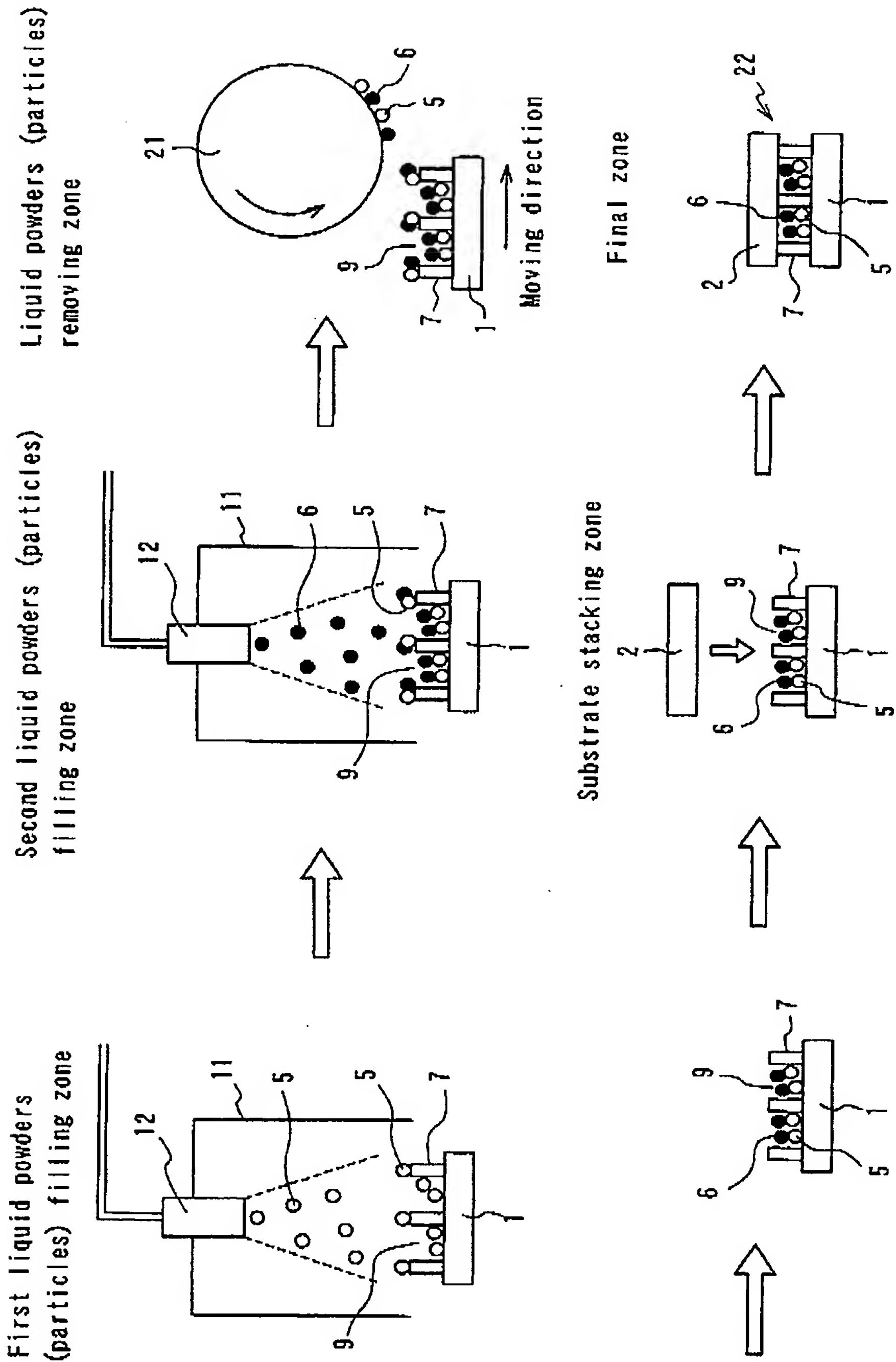


FIG. 11

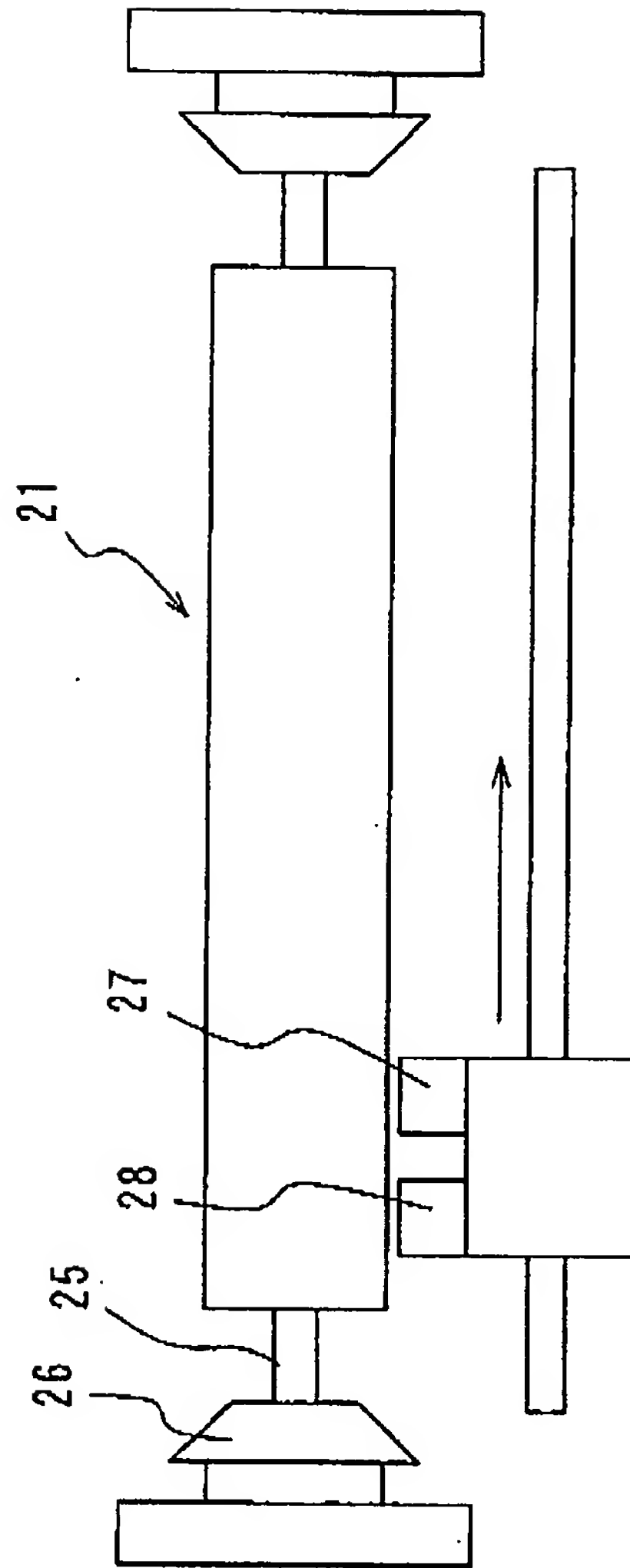
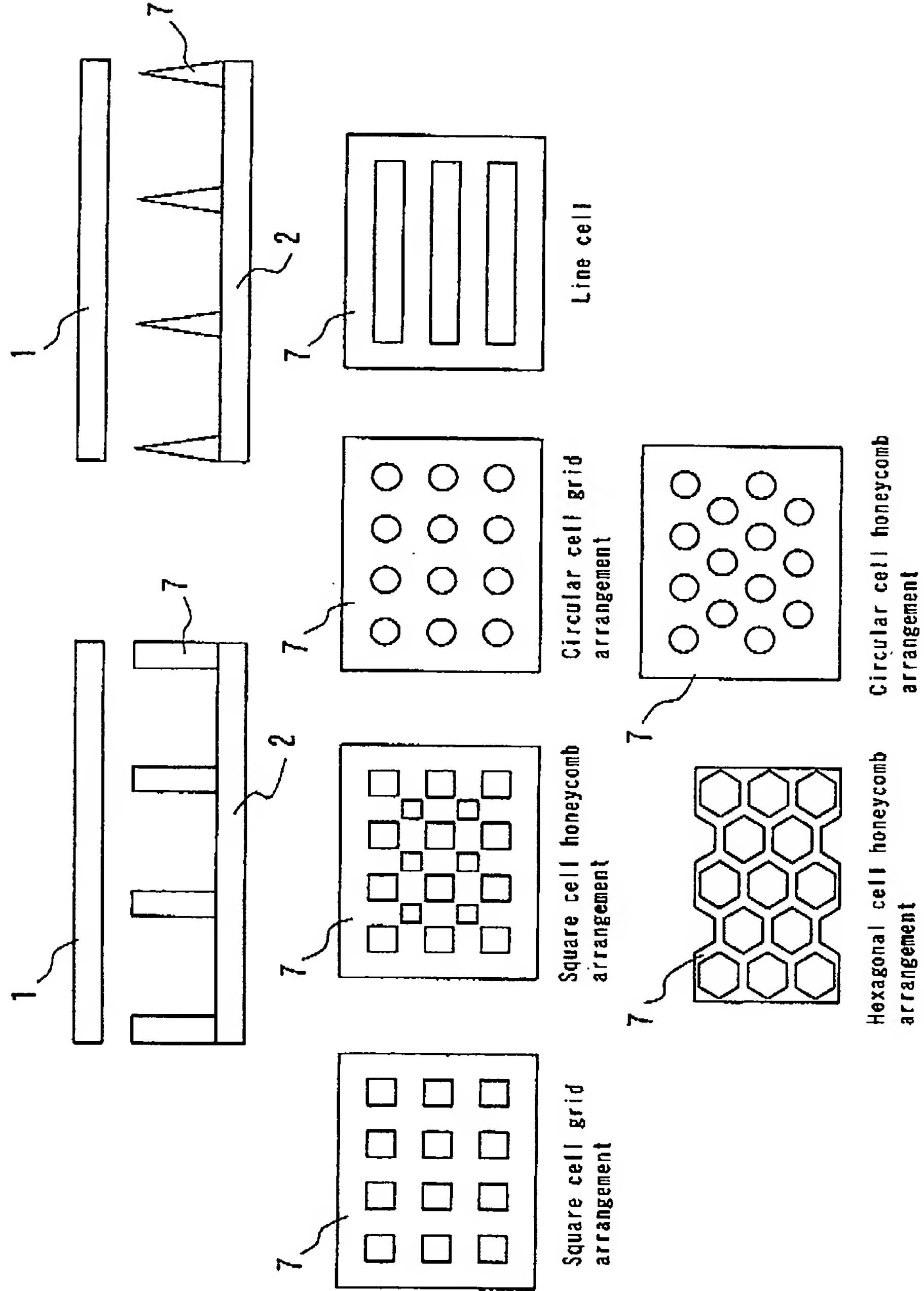
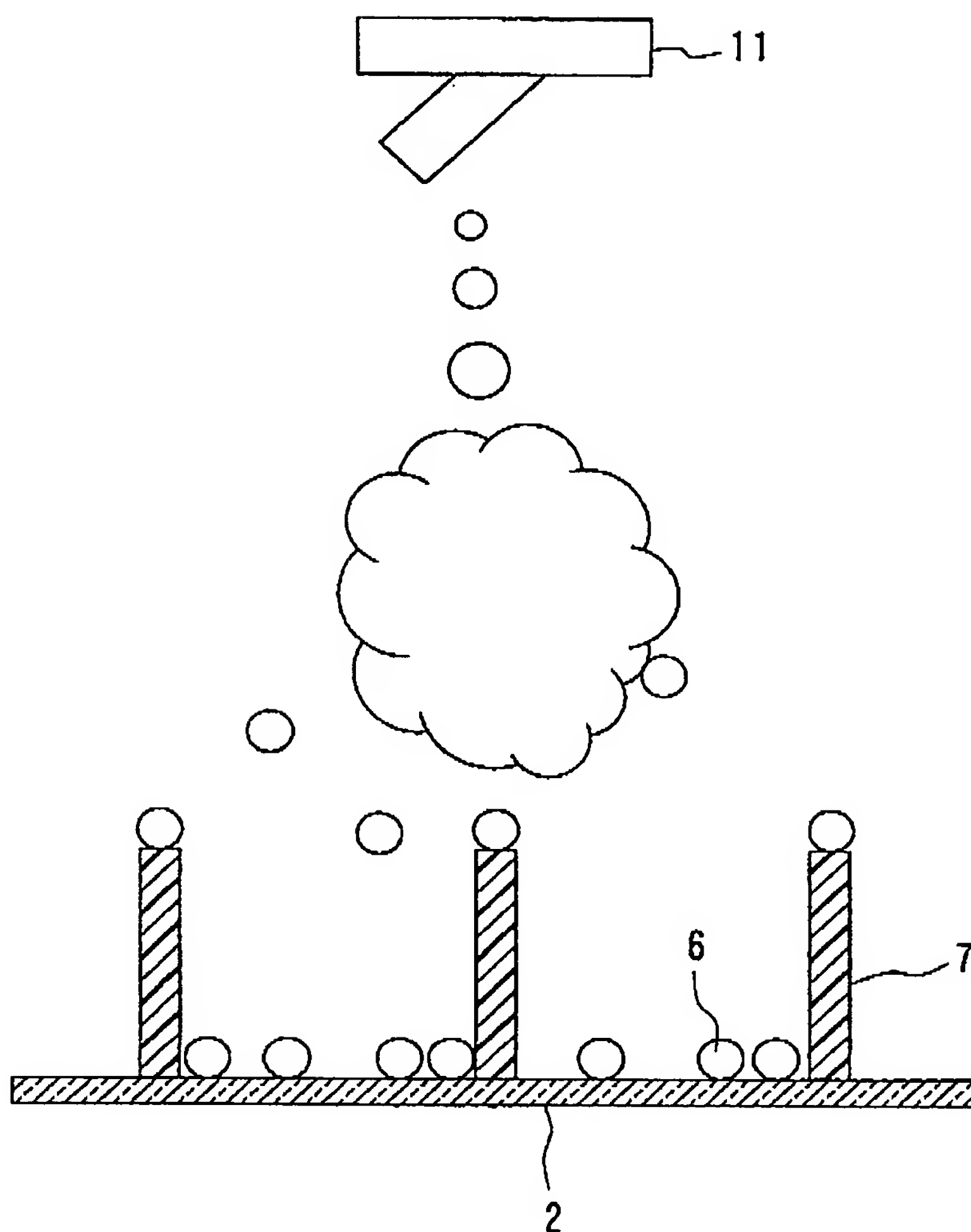


FIG. 12



**FIG. 13**

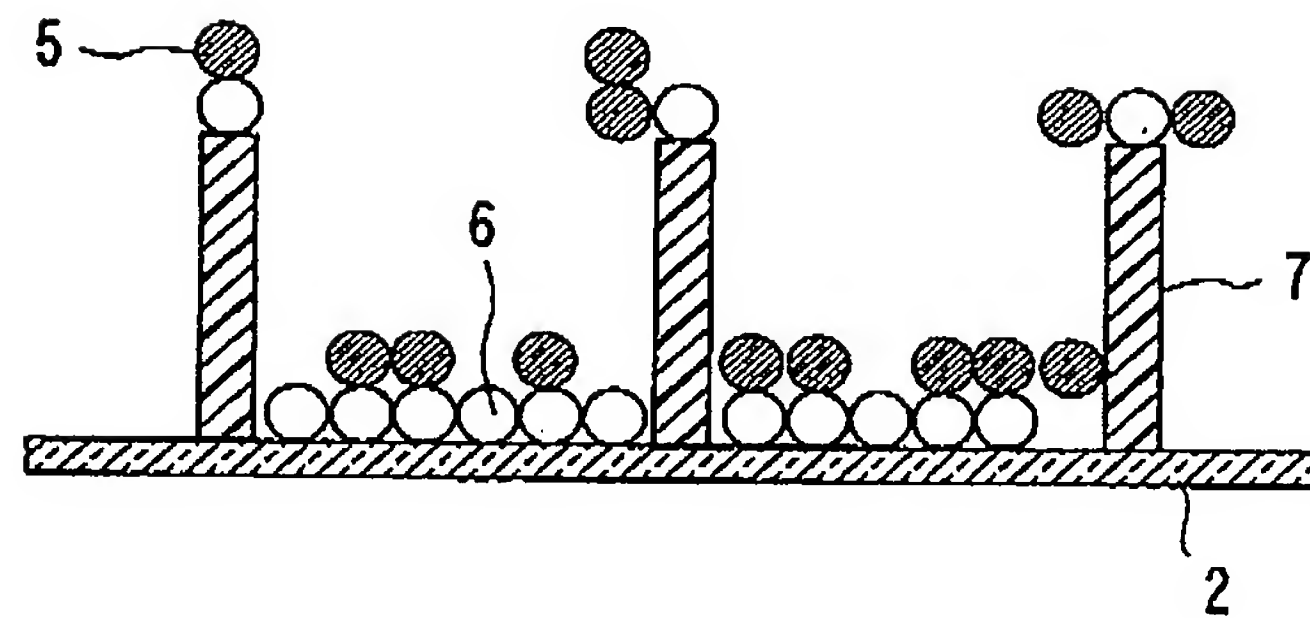
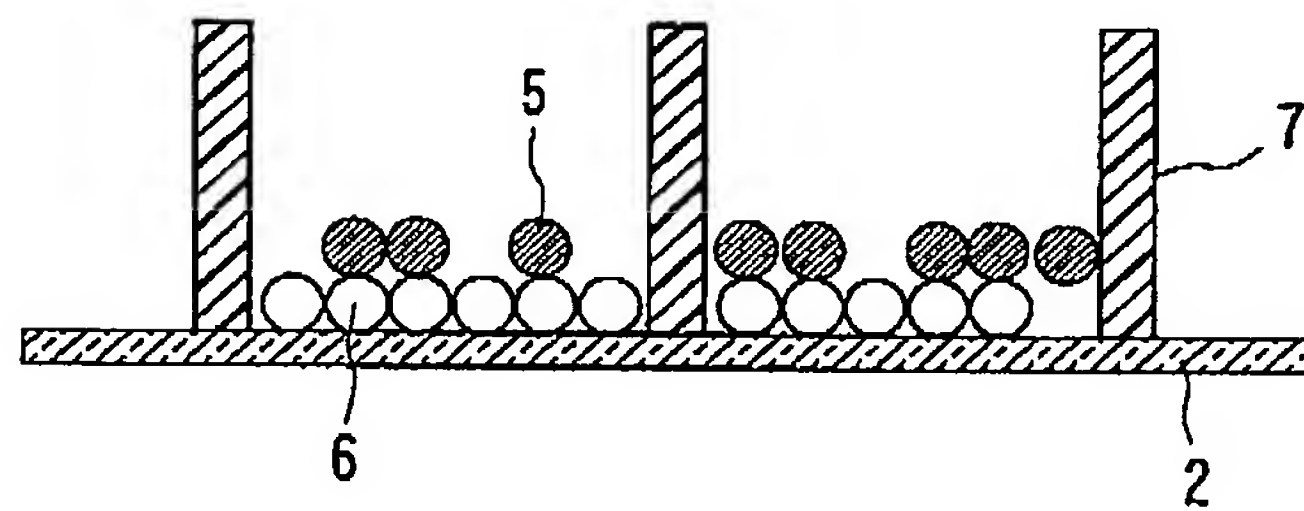
(1). Filling step



13/19

*FIG. 14a*

(2) Removing step

*FIG. 14b*

14/19

*FIG. 15a*

(3) Substrate stacking step

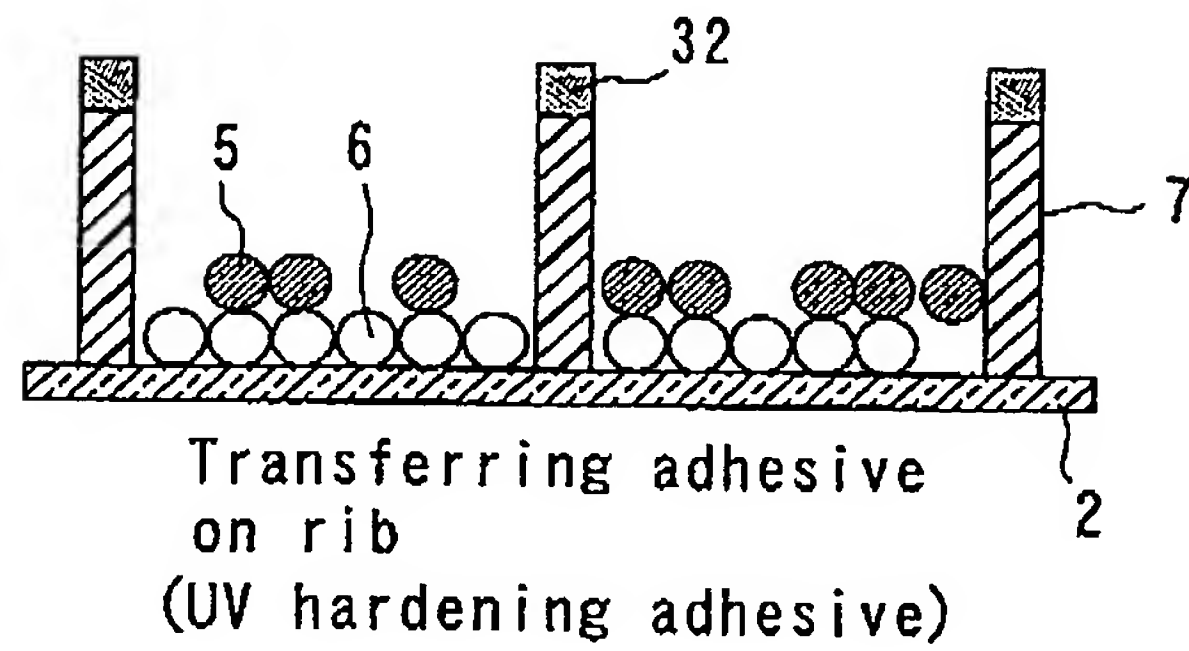
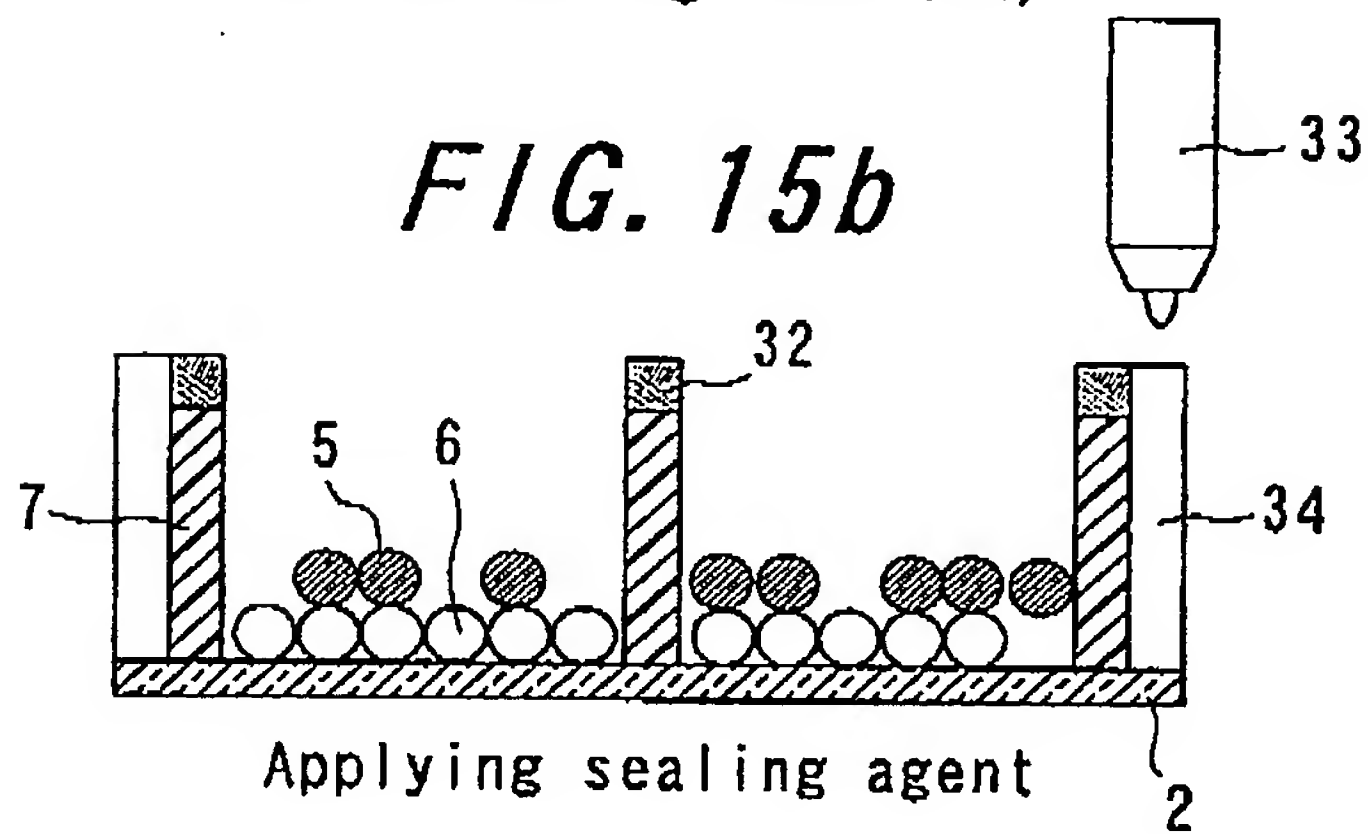
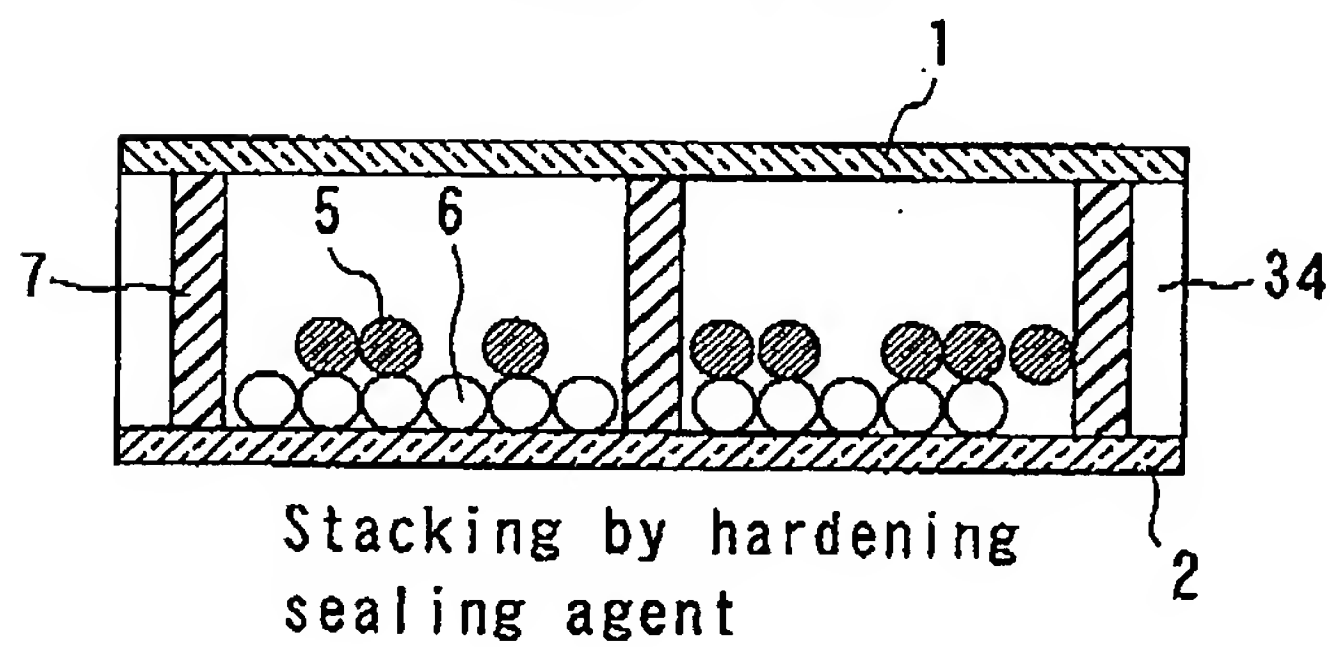
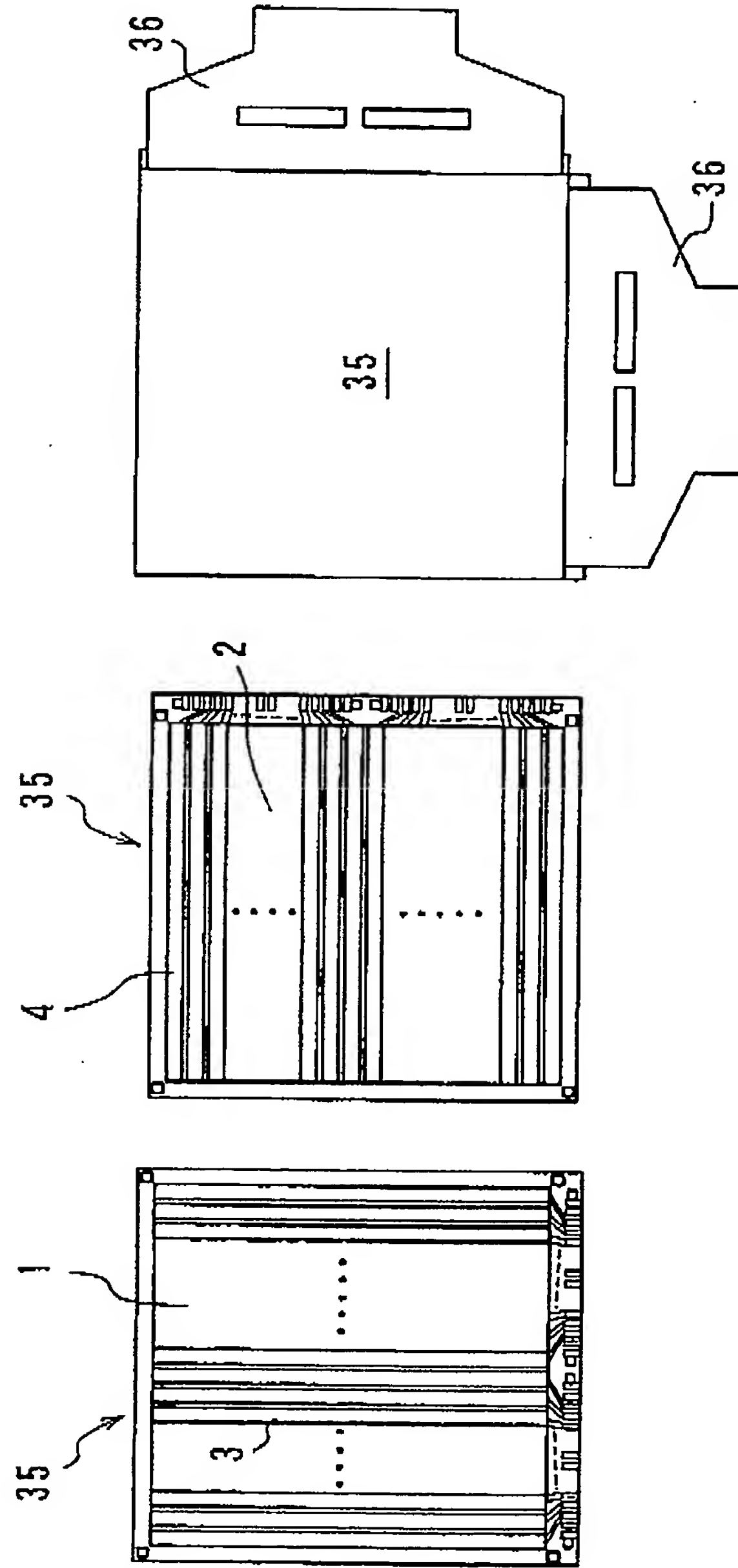
*FIG. 15b**FIG. 15c*

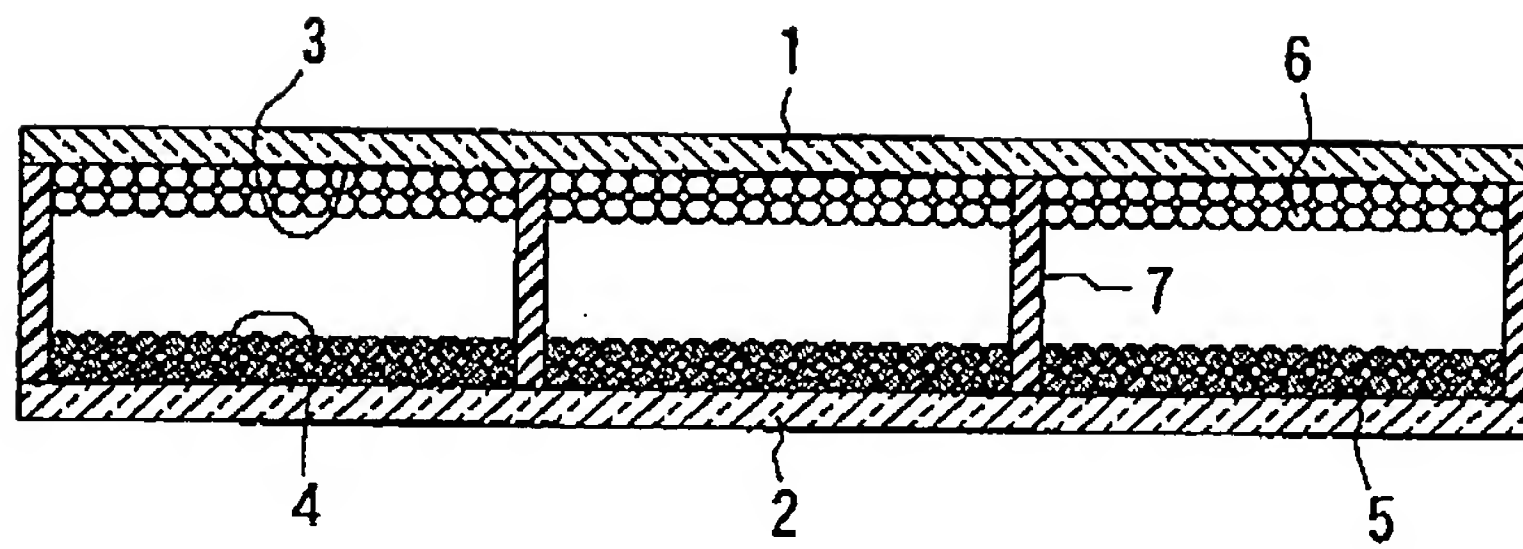
FIG. 16a      FIG. 16b      FIG. 16c

(4) or (5) electrode adhering step



16/19

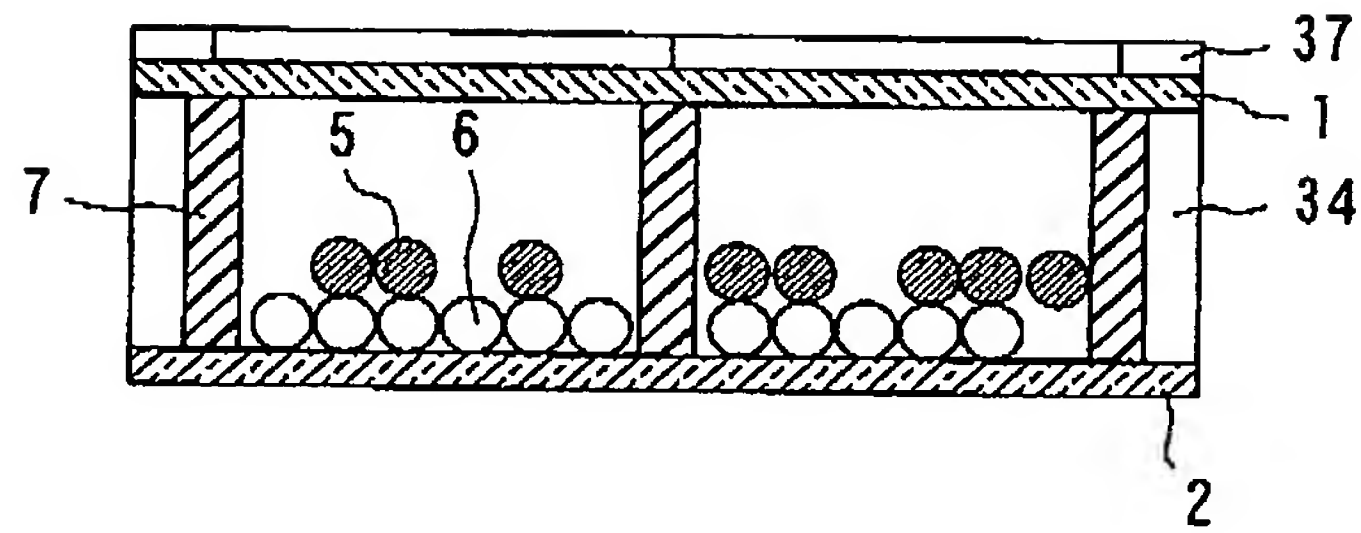
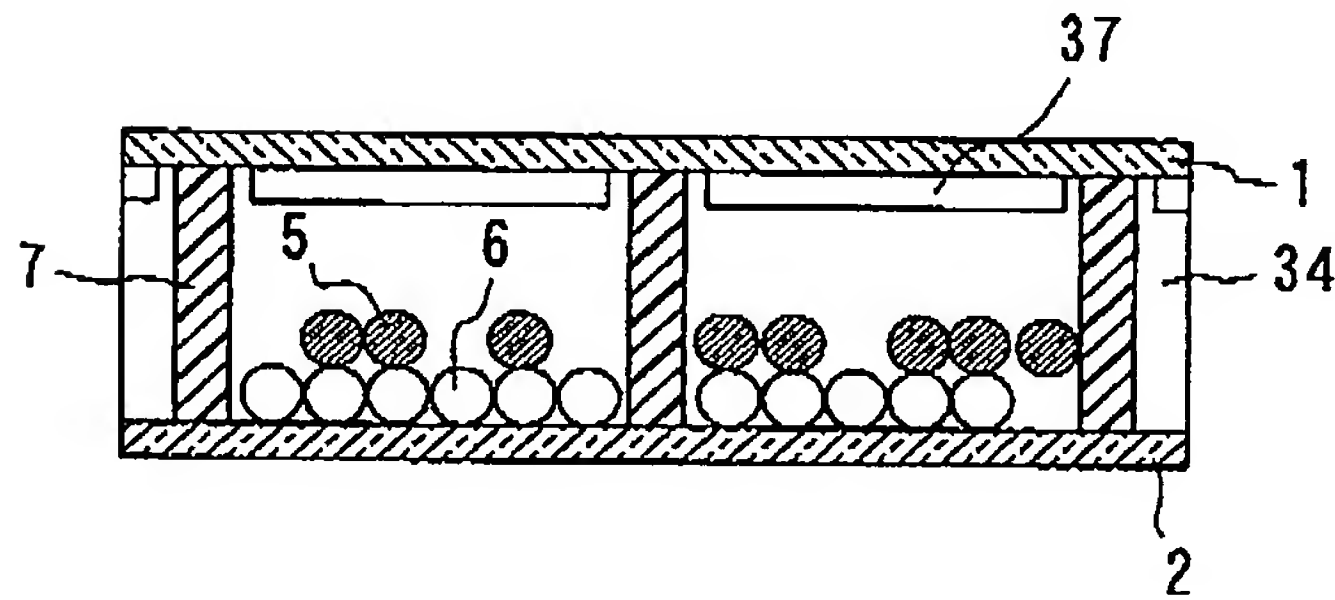
FIG. 17





*FIG. 18a*

(4) Color filter stacking step

*FIG. 18b*

18/19

FIG. 19

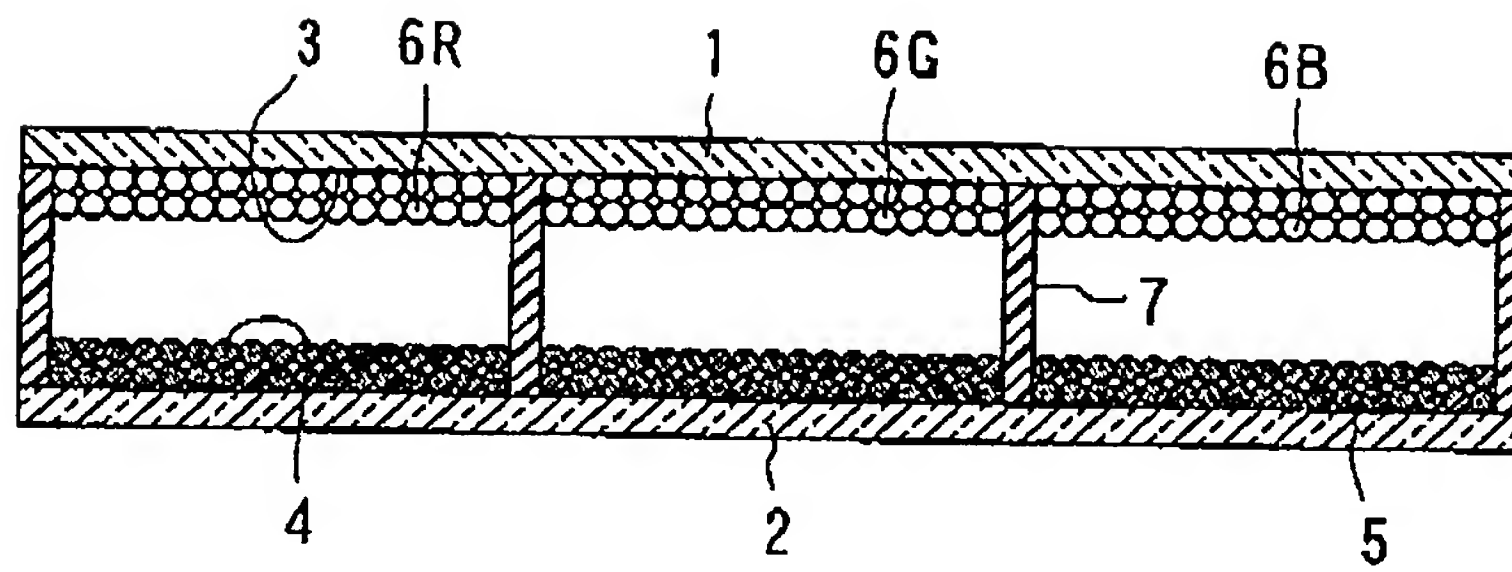


FIG. 20a

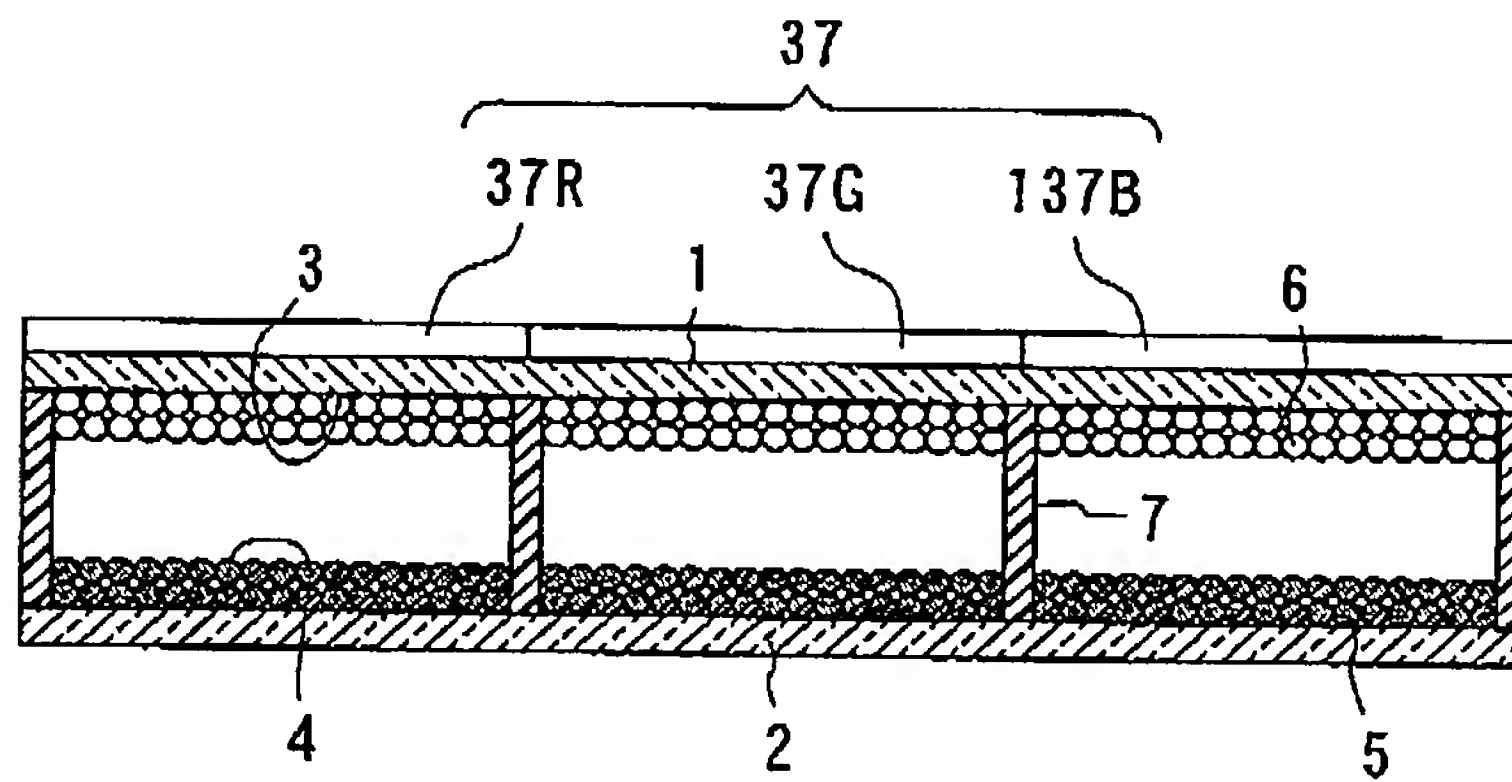
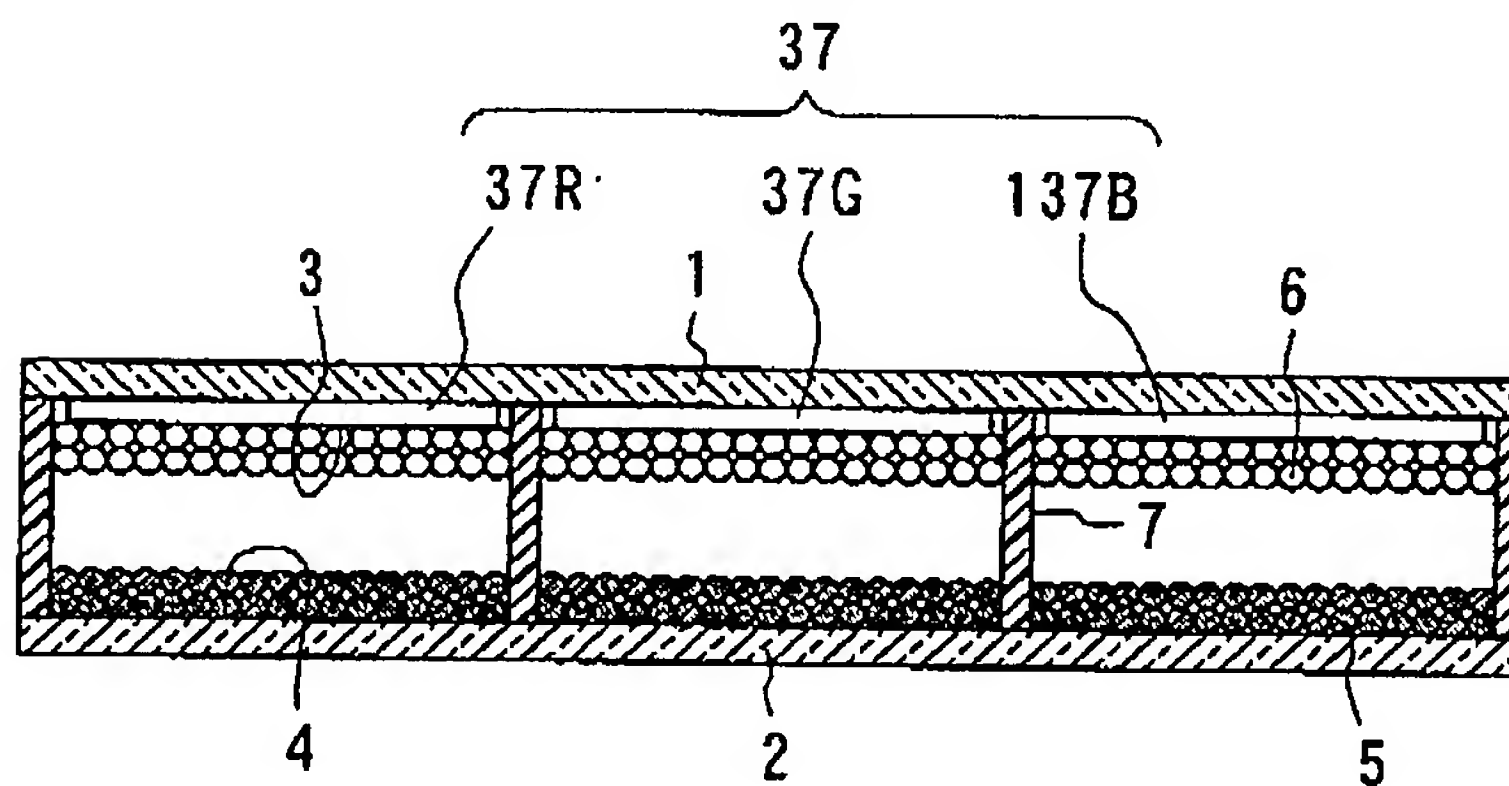


FIG. 20b



19/19

FIG. 21

